

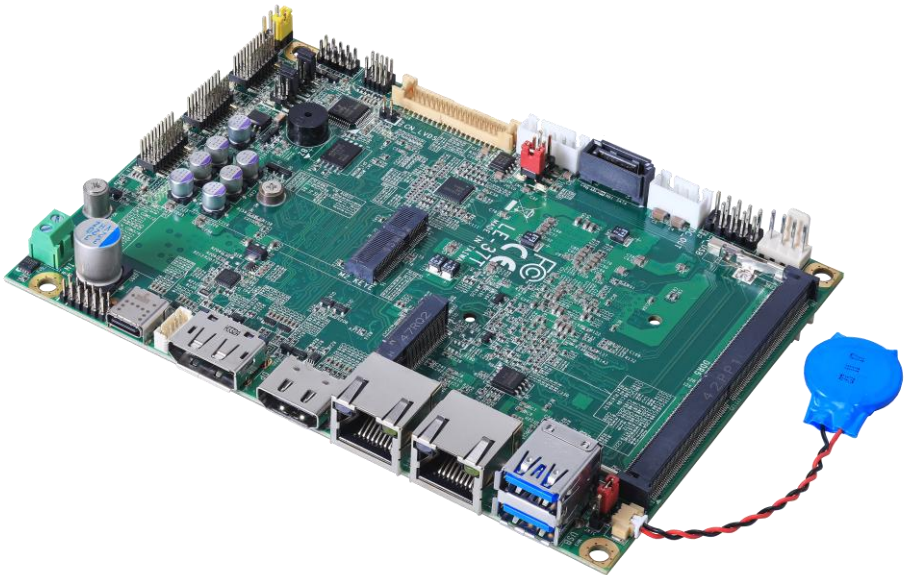
LE-37T

3.5 inch Motherboard

User's Manual

Edition 1.1

2025/04/21



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Packing List:

Please check the package content before you starting using the board.



1 x LE-37T 3.5 inch Miniboard
(Include Heat spreader)
(Optional) (Cooler fan for LE-37TX8)



1 x SATA Power Cable
(OALSATA15-2PJ / 1040613)
(Optional for OEM)



1 x Dual COM cable
(OALES-BKU2NB / 1040090)



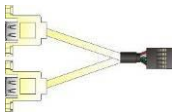
1 x Audio cable
(OALPJ-HDUNB / 1040123)



1 x SATA Cable
(OALSATA3-L) / (1040529)
(Optional for OEM)



1 x DC Input Power Cable
(OALDC-B / 1040513)



1 x USB2.0 cable
(OALUSBA-3 / 1040173)
(Optional)

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Chapter 1 <Introduction>

1.1 <Product Overview>

LE-37T is a 3.5" Motherboard which supports Atom™ Processor X Series, integrated Intel® Graphics, DDR5 memory, Realtek High Definition Audio, Intel Gigabit LAN, USB3.2 Gen2, SATA3, Type C with AHCI function for a system.

New feature for Amston Lake

Amston Lake Processor X Series have Intel UHD Graphics up to 32 EUs.

Integrated GPU for AI inference and media processing

All in One multimedia solution

The board provides one M.2 2230 slot, one M.2 2280 slot (PCIe Gen3), and supports Type C (Alt mode), HDMI, DP++, and High Definition Audio, to meet the very requirement of the multimedia application.

Amston Lake support Windows10 & 11 64bit and Linux

Intel recommends using Windows 10 & 11 64bit. It may lose some drivers if you use other Windows version.

Note

Refer to OS Vendor site for more information regarding latest OS revision support.

1.2 <Product Specification>

System

Processor	Intel® Atom™ Processor X Series(Amston Lake) FCBGA1264 package
Memory	1 x DDR5 SO-DIMM up to 4800 MHz 32GB, Support Non-ECC, unbuffered memory
Watchdog Timer	Generates a system reset with internal timer for 1min/s ~ 255min/s
Real Time Clock	Chipset integrated RTC with onboard lithium battery
Expansion	1 x M.2 2230 Key E for Wi-Fi and Bluetooth 2 x M.2 2280 Key M support Gen3 PCIe x4 for NVMe

Graphics

Chipset	Intel® UHD Graphics
Display Interface	1 x LVDS (Note1) , 1 x DisplayPort ++, 1 x HDMI, 1 x Type-C (DP Alt. Mode)

LAN

Chip	2 x Intel® I226 Gigabit LAN (up to 2.5GbE)
------	--

I/O

Audio	Realtek ALC888S HD Audio
Internal I/O	1 x SATA, 2 x USB2.0, 4 x RS232, 2 x RS232/422/485 1 x LVDS, 1 x LCD inverter connector, 1 x GPIO, 1 x Audio, 1 x SMBus, 1 x Speaker_Out with Amplifier(Note2) 1 x SATA(Note3)
Rear I/O	1 x DisplayPort ++, 1 x HDMI, 1 x Type-C (DP Alt. Mode), 2 x USB3.2 Gen2, 2 x LAN

Mechanical & Environmental

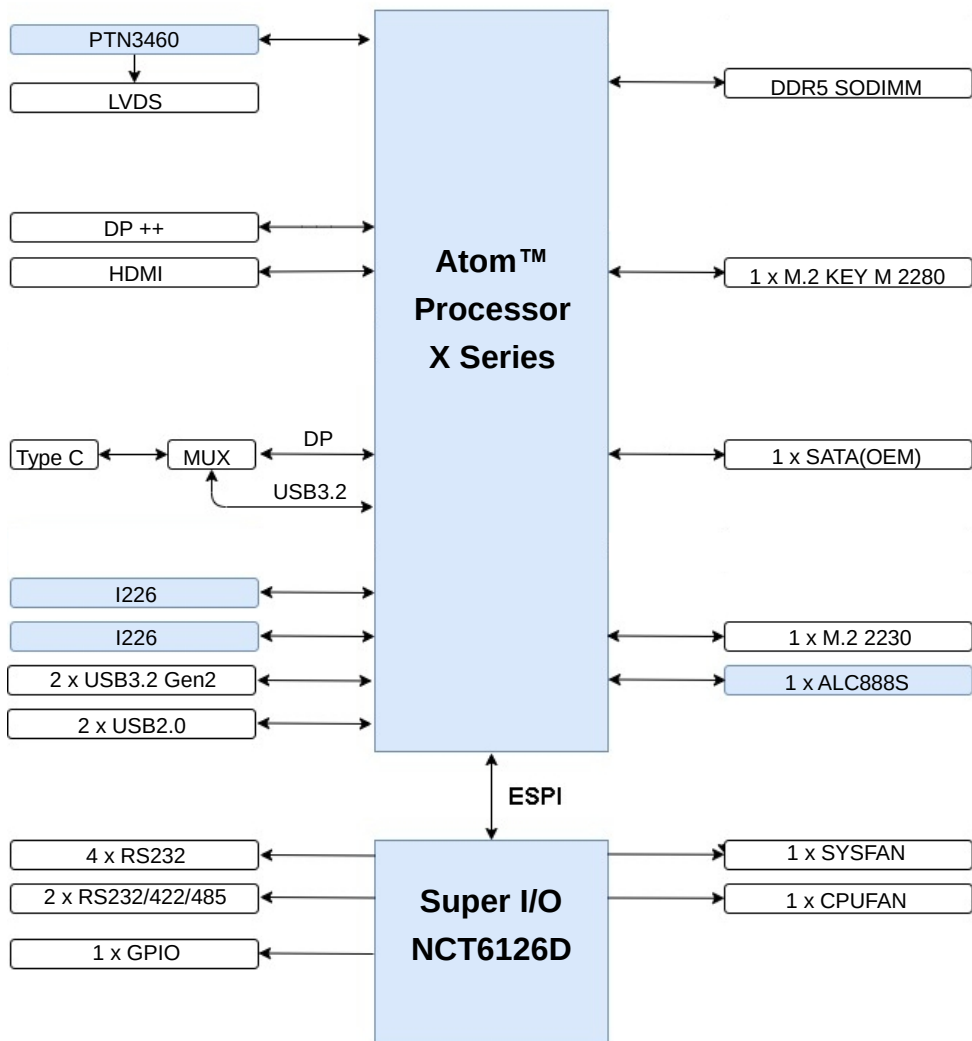
Power Requirement	DC input 9~35V
Size	146mm x 101mm (L x W)
Temperature	Operating within -40°C~85°C (-40°F~185°F) Storage within -40°C~85°C (-40°F~185°F)
Relative Humidity	10%~90%, non-condensing

Note1: Onboard 18/24-bit single/dual channel +3.3V/ +5V/ +12V LVDS.

Note2: Speaker_Out with 3Watt for 4ohm speaker

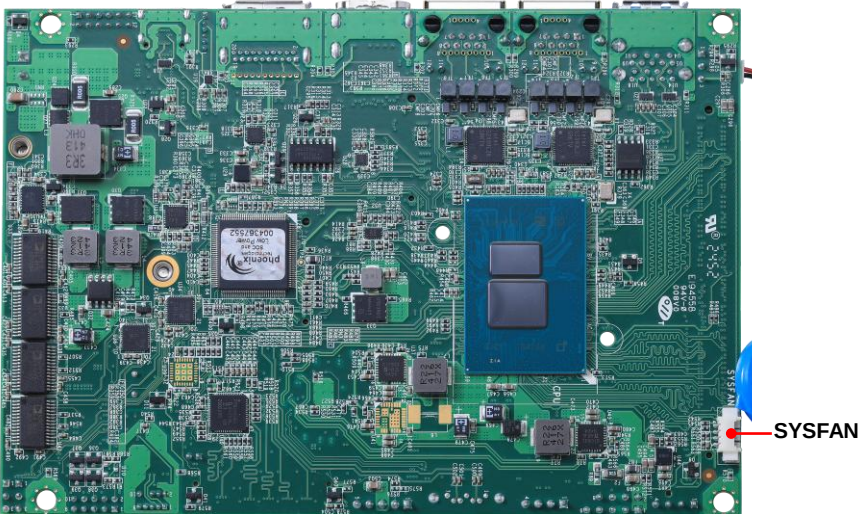
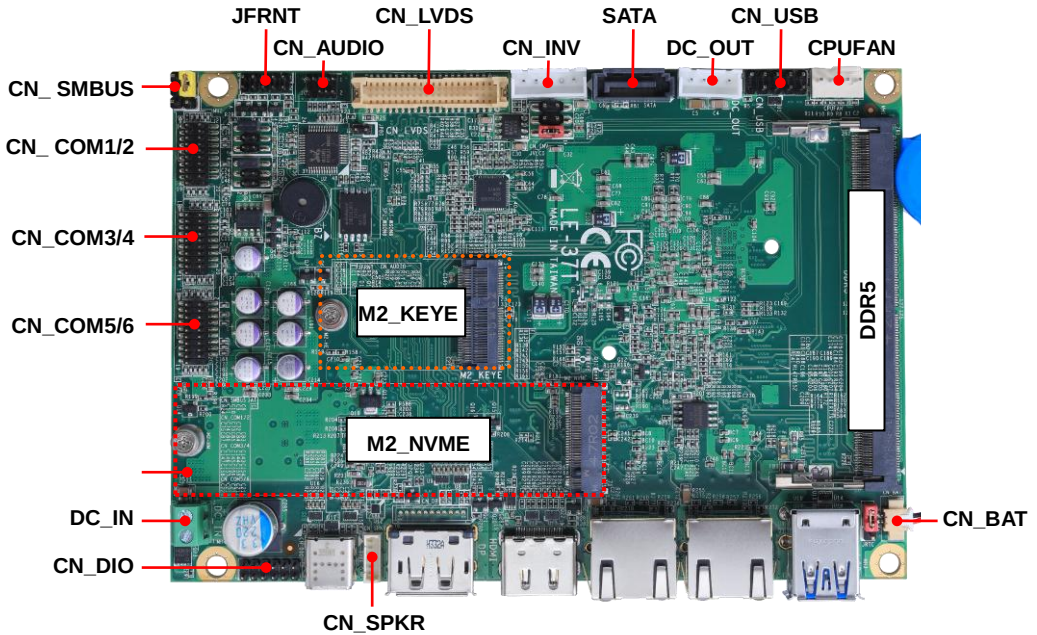
Note3: SATA port for OEM

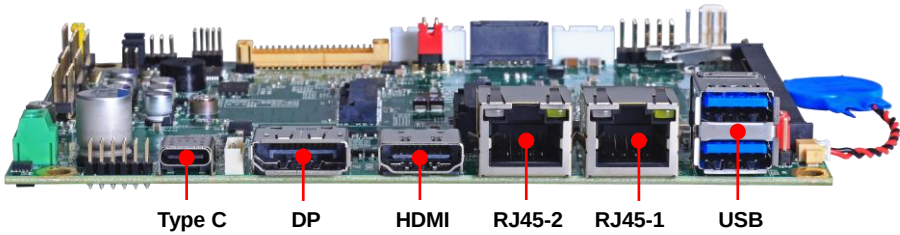
1.3 <Block Diagram>



Chapter 2 <Hardware setup>

2.1 <Connector Location and Reference>





2.1.1 <Internal connectors list>

Connector	Function
DDR5	262-pin DDR5 SO-DIMM slot
SATA	7-pin SATA connector (OEM)
CN_AUDIO	5 x 2-pin audio pin header
CN_LVDS	20 x 2-pin LVDS connector
CN_INV	5-pin LCD inverter connector
CN_SMBUS	3-pin SMBus connector
CN_COM 1/2	20-pin RS232/RS422/RS485 connector
CN_COM 3/4	20-pin RS232 connector
CN_COM 5/6	20-pin RS232 connector
CN_USB	5 x 2-pin USB2.0 pin header
CN_DIO	6 x 2-pin digital I/O connector
CN_BAT	2-pin Battery connector
CN_SPKR	6-pin Speaker connector
CPUFAN	4-pin CPU fan connector
SYSFAN	4-pin system fan connector
JFRNT	10-pin front panel switch/indicator connector
M2_KEYE	75-pin M.2 Key E slot
M2_NVME	75-pin M.2 2280 Key M slot support Gen3 PCIe x4
DC_IN	2-pin power input Terminal Block
DC_OUT	4-pin SATA Power connector

2.1.2 <External connectors list>

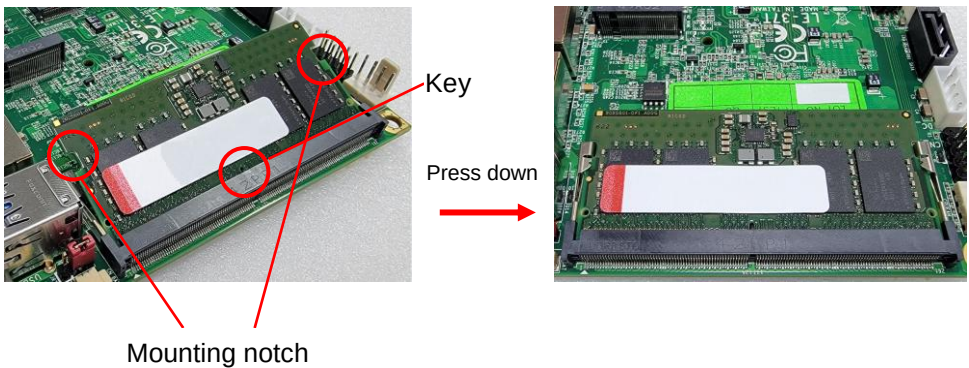
Connector	Function
HDMI	HDMI connector
DP	DisplayPort ++ connector
USB	2 x USB3.2 Gen2 connector
RJ45-1	RJ45 connector (I226)
RJ45-2	RJ45 connector (I226)
Type C	Support USB3.2 Gen2 or DP

2.2 <Memory Setup>

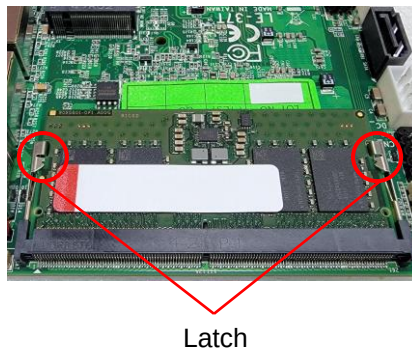
LE-37T has 262-pin DDR5 SODIMM support up to 32GB of memory capacity and 1.1 Voltage. Only Non-ECC memory is supported.

In the process, the board must be powered off.

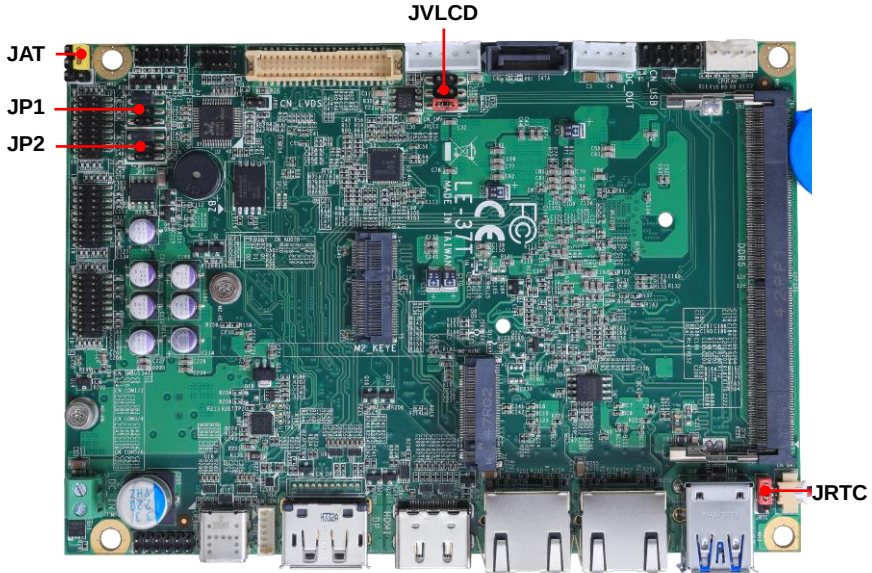
1. Put the memory tilt into the slot. Note the Memory notch key aligned slot key.
2. Then press down till lock into the mounting notch.



3. To remove the memory, push outward on both sides of the latch.



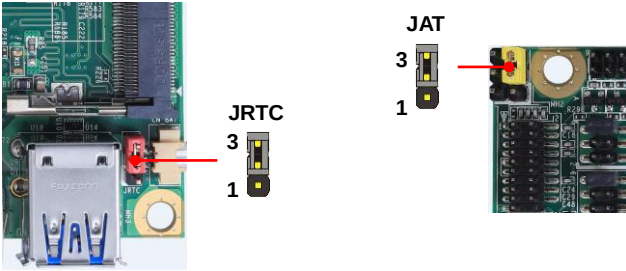
2.3 <Jumper Location and Reference>



2.3.1 <Jumper list>

Jumper	Function
JAT	Power mode select
JRTC	CMOS Normal/Clear Setting
JVLCD	Panel Voltage Setting
JP1	COM1 Voltage Setting (For Pin 9)
JP2	COM2 Voltage Setting (For Pin 9)

2.3.2 <Clear CMOS and Power on type selection>



JAT: AT/ATX mode select jumper

Jumper settings	Function
1-2	AT mode
2-3	ATX mode (Default)

JRTC: Clear CMOS data jumper

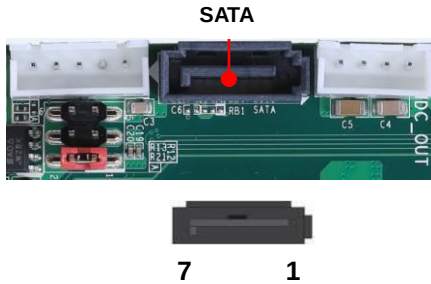
Jumper settings	Function
1-2	Clear CMOS
2-3	Normal (Default)

2.4 <I/O interface>

2.4.1 <Serial ATA interface>

SATA : SATA 7-pin connector

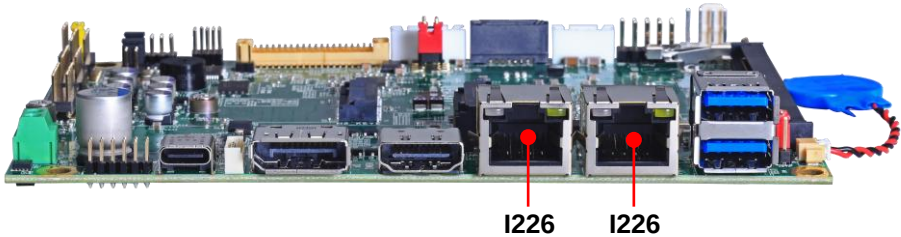
Pin	Signal
1	GND
2	TX+
3	TX-
4	GND
5	RX-
6	RX+
7	GND



SATA port for OEM

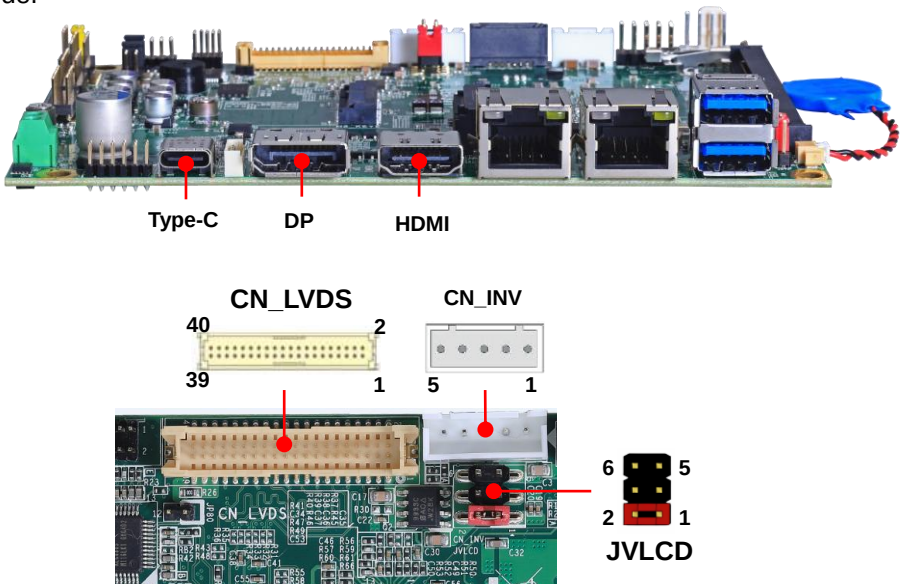
2.4.2 <Ethernet interface>

The board provides I226 2.5Gigabit Ethernet which supports WOL on rear I/O.



2.4.3 <Display interface>

Based on the Atom™ CPU (X Series Platform) with built-in Intel® UHD Graphics, the DisplayPort ++ resolution up to 4096x2160 @ 60Hz, the HDMI up to 3840x2160 @ 24Hz , the Type-C(DP Alt. Mode) and LVDS up to 1920x1080 @ 60Hz supports single bus or dual bus LVDS signaling with color depths of 18 bits or 24 bits. About select LCD Panel Type in BIOS, please refer [Appendix A](#). The built-in Graphics support Triple display function with clone mode and extended mode.



CN_LVDS: LVDS 40-pin connector (Model: HIROSE DF13-40DP-1.25V compatible)

Pin	Signal	Pin	Signal
2	Set by JVLCD	1	Set by JVLCD
4	Detect (Active low)	3	GND
6	A_LVDS_0-	5	B_LVDS_0-
8	A_LVDS_0+	7	B_LVDS_0+
10	GND	9	GND
12	A_LVDS_1-	11	B_LVDS_1-
14	A_LVDS_1+	13	B_LVDS_1+
16	GND	15	GND
18	A_LVDS_2-	17	B_LVDS_2-
20	A_LVDS_2+	19	B_LVDS_2+
22	GND	21	GND
24	A_LVDS_CLK-	23	B_LVDS_3-
26	A_LVDS_CLK+	25	B_LVDS_3+
28	GND	27	GND
30	A_LVDS_3-	29	B_LVDS_CLK-
32	A_LVDS_3+	31	B_LVDS_CLK+
34	GND	33	GND
36	LVDS_DDCSCL	35	NC
38	LVDS_DDCSDA	37	NC
40	NC	39	NC

Pin4 only need to be connected to GND

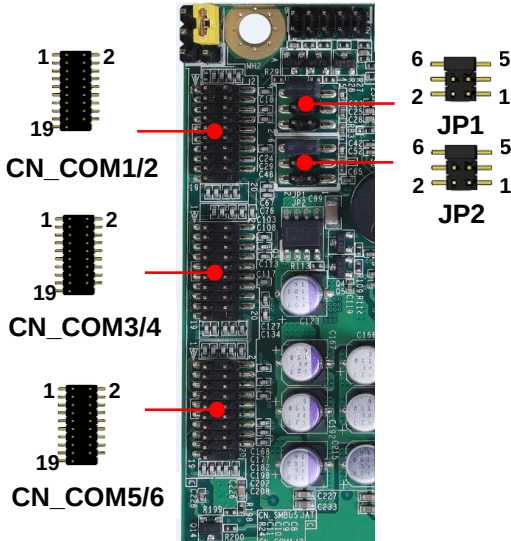
CN_INV: LVDS 5-pin Backlight power connector

Pin	Signal
1	12V
2	Backlight Control
3	5V
4	GND
5	Enable Backlight

JVLCD: LVDS panel power select jumper

Jumper settings	Function
1-2	3.3V (Default)
3-4	5V
5-6	12V

2.4.4 <Serial Port interface>



CN_COM1/2: RS232/422/485 20-pin header (Pitch 2.54 x 1.27mm)

Pin	Signal	Pin	Signal
1	DCD1/ 422TX-/ 485-	2	RXD1/ 422TX+/ 485+
3	TXD1/ 422RX+	4	DTR1/ 422RX-
5	GND	6	DSR1
7	RTS1	8	CTS1
9	Set by JP1	10	NC
11	DCD2/ 422TX-/ 485-	12	RXD2/ 422TX+/ 485+
13	TXD2/ 422RX+	14	DTR2/ 422RX-
15	GND	16	DSR2+
17	RTS2	18	CTS2
19	Set by JP2	20	N/A

CN_COM1/2 RS-232/422/485 can set by BIOS.

You can find the setting from

Advanced-> Motherboard Advanced menu-> Super IO configuration->

Serial Port configuration->Interface

Phoenix SecureCore Technology Setup	
Advanced	
Serial Port Configuration	
Serial Port 1 [Enabled] Base I/O Address: [3F8/IRQ4] Interface: [RS232] Mode Speed Select [RS232/RS422/RS485=250Kbps] Clock Source [1.0462 MHz (115200)] 128 Bytes FIFO [Disabled]	Item Specific Help RS232,RS485 and RS422 Select

JP1, JP2: COM1, COM2 pin-9 setting

Jumper settings	Function
1-2	5V
3-4	12V
5-6	RI (Default)

Effective patterns of connection:

1-2 / 3-4 / 5-6

Other may cause damage

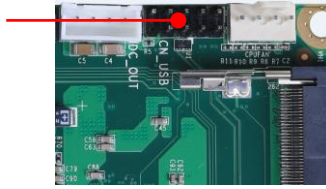
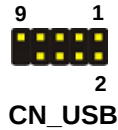
CN_COM3/4: RS232 20-pin header (Pitch 2.54 x 1.27mm)

Pin	Signal	Pin	Signal
1	DCD3	2	RXD3
3	TXD3	4	DTR3
5	GND	6	DSR3
7	RTS3	8	CTS3
9	RI3	10	NC
11	DCD4	12	RXD4
13	TXD4	14	DTR4
15	GND	16	DSR4
17	RTS4	18	CTS4
19	RI4	20	N/A

CN_COM5/6: RS232 20-pin header (Pitch 2.54 x 1.27mm)

Pin	Signal	Pin	Signal
1	DCD5	2	RXD5
3	TXD5	4	DTR5
5	GND	6	DSR5
7	RTS5	8	CTS5
9	RI5	10	NC
11	DCD6	12	RXD6
13	TXD6	14	DTR6
15	GND	16	DSR6
17	RTS6	18	CTS6
19	RI6	20	N/A

2.4.5 <USB interface & Type C >

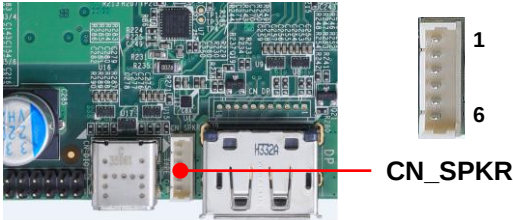
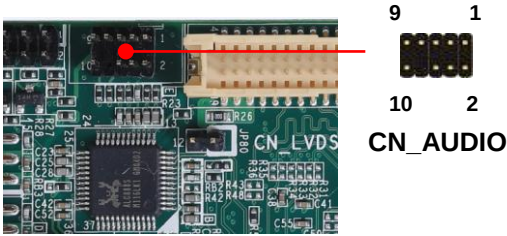


CN_USB: USB2.0 10-pin header (Pitch 2.54 mm)

Pin	Signal	Pin	Signal
1	5VSB	2	5VSB
3	DATA0-	4	DATA1-
5	DATA0+	6	DATA1+
7	GND	8	GND
9	GND	10	N/A

Type C supports USB 3.2 Gen2 and DP Alt. mode, and provides 5V at 3A.

2.4.6 <Audio interface>



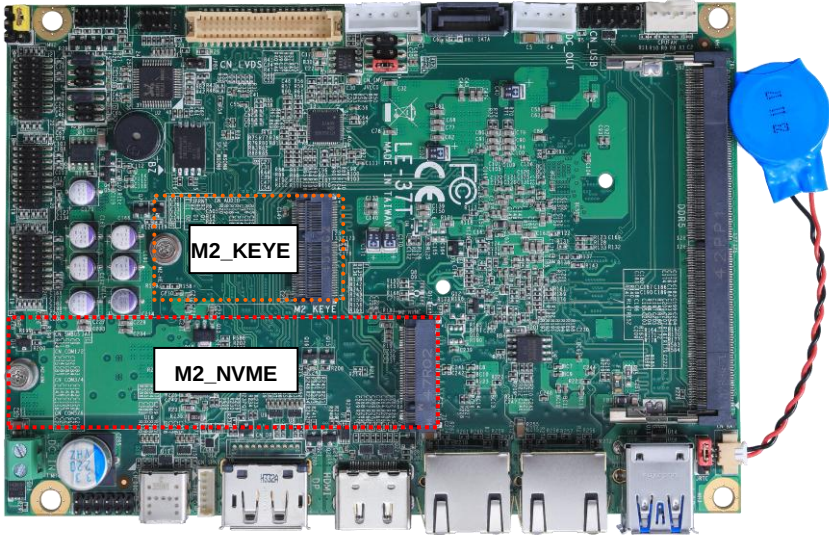
CN_AUDIO: Front panel audio 10-pin header (Pitch 2.54*1.27mm)

Pin	Signal	Pin	Signal
1	MIC_L	2	GND
3	MIC_R	4	NC
5	FP_OUT_R	6	MIC_DETECT
7	SENSE	8	N/A
9	FP_OUT_L	10	FP_OUT_DETECT

CN_SPKR: 6-pin Two channel Speaker_Out with Amplifier

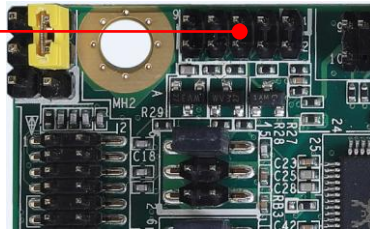
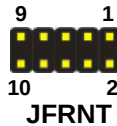
Pin	Signal	Pin	Signal
1	FP_OUT_L_P	2	FP_OUT_L_N
3	JD_Front	4	GND
5	FP_OUT_R_P	6	FP_OUT_R_N

2.4.7 <Expansion slot>



M2_KEYE with 1 x PCI Express x1 support WI-FI and Bluetooth Module
M.2_NVME support Gen3 PCIe x4

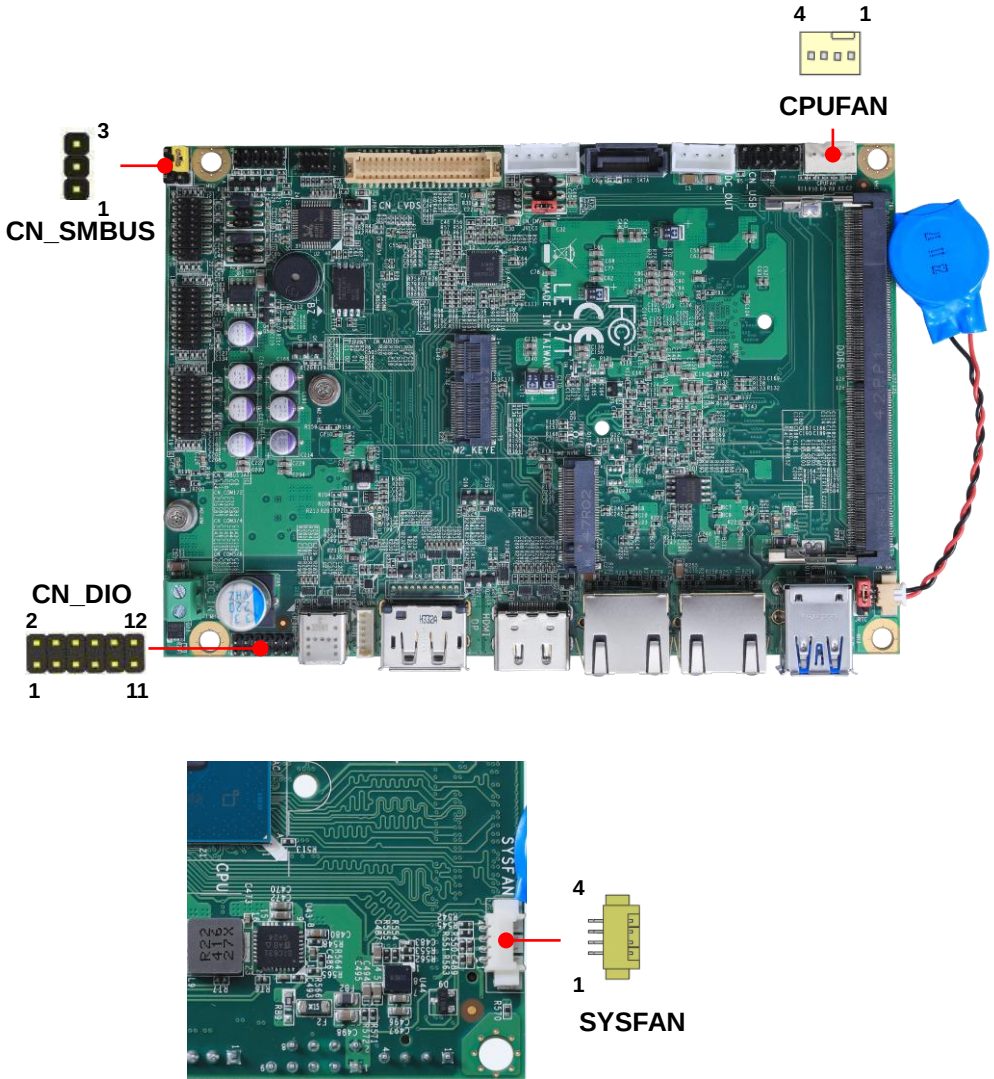
2.4.8 <Front panel switch and indicator>



JFRNT: Front panel switch and indicator 10-pin header

Pin	Signal	Pin	Signal
1	Power_ON-	2	Power_ON+
3	Speaker-	4	Speaker+
5	HDD_LED-	6	HDD_LED+
7	Power_LED-	8	Power_LED+
9	Reset+	10	Reset-

2.4.9 <GPIO and Other interface>



When using GPIO function, please note:

As Output: **Open-drain**, most applications need use an external pull up resistor.

(If not may cause damage)

As Input: **TTL-level**.

GPIO DC characteristics (open drain mode)

Parameter	SYM	MIN	TYP	MAX	UNIT	Conditions
Input Low Voltage	V_{IL}			0.8	V	
Input High Voltage	V_{IH}	2.0			V	
Output Low Voltage	V_{OL}			0.4	V	$I_{OL} = 12\text{mA}$
Input High Leakage	I_{LH}			+10	μA	$V_{IN} = 3.3\text{V}$
Input Low Leakage	I_{LIL}			-10	μA	$V_{IN} = 0\text{V}$

Please refer to [Appendix D](#) to program the configuration register

CN_DIO: GPIO 12-pin header (Pitch 2.00mm)

Pin	Signal	Pin	Signal
1	GND	2	GND
3	GP0	4	GP4
5	GP1	6	GP5
7	GP2	8	GP6
9	GP3	10	GP7
11	5V	12	N/A

CN_SMBUS: SMBus 5-pin connector (Pitch 2.54mm)

Pin	1	2	3
Signal	SMBCLK	GND	SMBDAT

CPUFAN: CPU cooler fan 4-pin connector

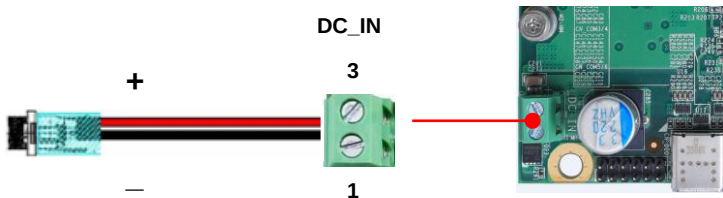
Pin	1	2	3	4
Signal	GND	12V	Sensor	Control

SYSFAN: System cooler fan 4-pin connector

Pin	1	2	3	4
Signal	GND	12V	Sensor	Control

2.5 <Power supply>

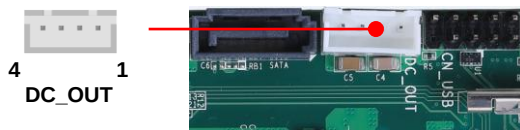
2.5.1 <Power input>



DC_IN: Terminal block 2-pin power connector

Pin	Signal	Pin	Signal
1	GND	3	DC input 9~35V

2.5.2 <Power Output>



DC_OUT: SATA power 4-pin connector

Pin	Signal
1	12V
2	GND
3	GND
4	5V

Appendix A <LCD Panel Type select>

According your panel, it needs to select the correct resolution in the BIOS. If there is no fit your panel type, please feedback for us to make OEM model.

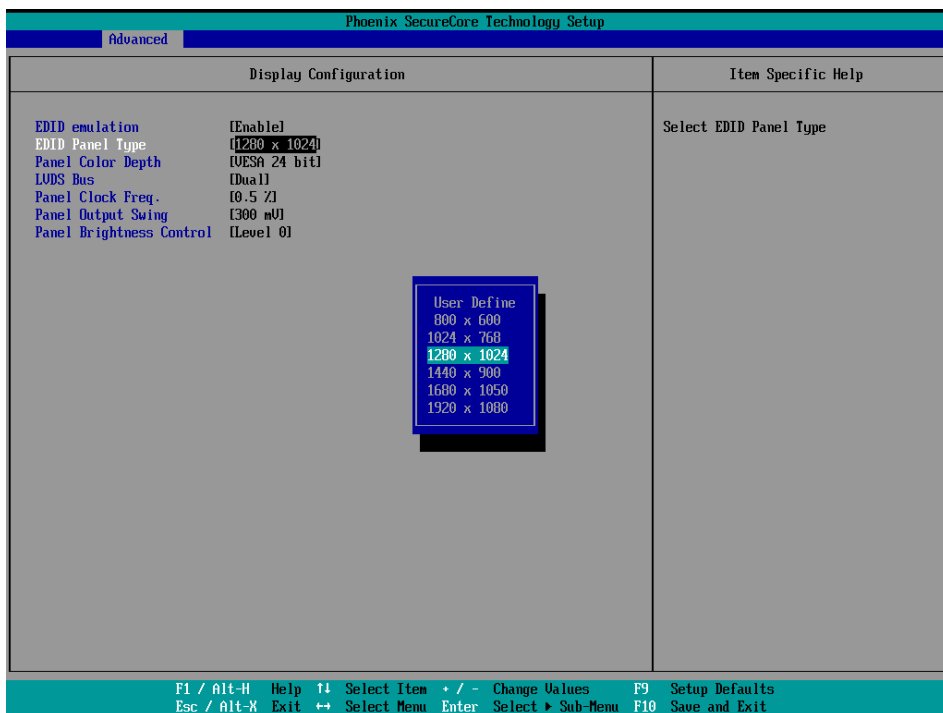
Find the setting from

Advanced->Motherboard Advanced menu->LVDS Configuration

EDID Panel type: There are 7 resolutions in LCD Panel Type, if your panel is not in the list, please contact tech@commell.com.tw

LVDS Bus: Select Single / Dual channel

Panel Color Depth: Select VESA 24 bit / JEIDA 24 bit / VESA and JEIDA 18 bit



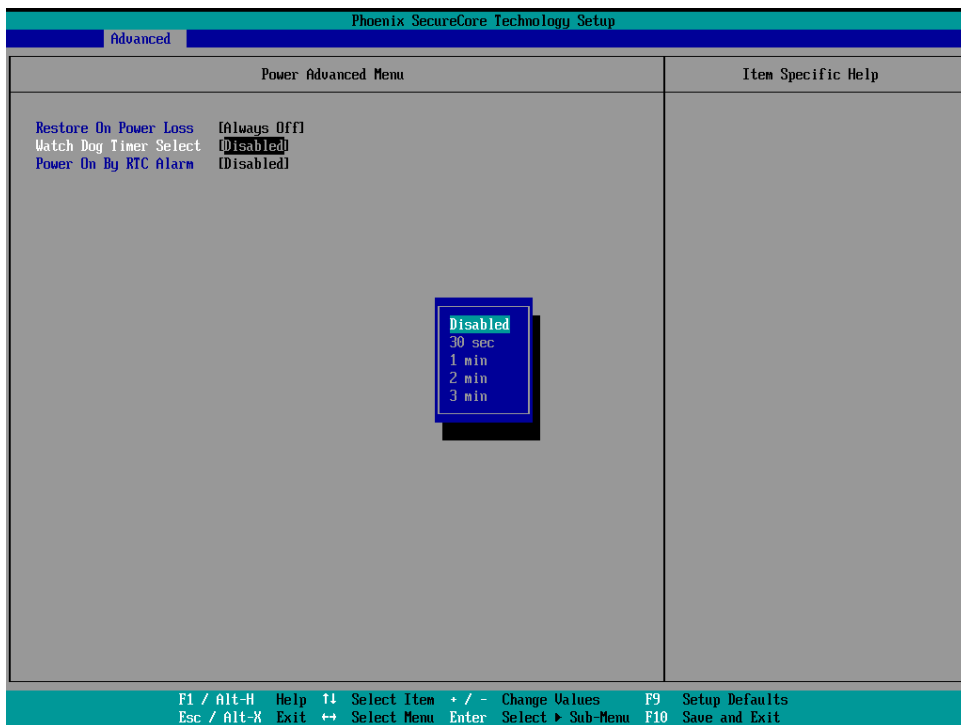
Appendix B <Programmable Watch Dog Timer>

The watchdog timer makes the system auto-reset while it stops to work for a period. The integrated watchdog timer can be setup as system reset mode by program. You can select Timer setting in the BIOS, after setting the time options, the system will reset according to the period of your selection.

Find the setting from

Advanced→Motherboard Advanced Menu->Power Advanced menu->

Watch dog timer select



Program sample

Watchdog timer setup as system reset with 5 second of timeout

```
-o 4E 87      ;enter configuration
-o 4E 87
-o 4E 07
-o 4F 08      ;select Logical Device
-o 4E 30
-o 4F 01      ; activate WDTO# function
-o 4E F0
-o 4F 00      ;set "00" is second mode, set "08" is minute mode
-o 4E F1
-o 4F 05      ;00h: Timeout Disable
                ;01h: Timeout occurs after 1 minute only
                ;02h: Timeout occurs after 2 second/minute
                ;03h: Timeout occurs after 3 second/minute
                :
                ;FFh: Timeout occurs after 255 second/minute
                (The deviation is approx 1 second.)
```

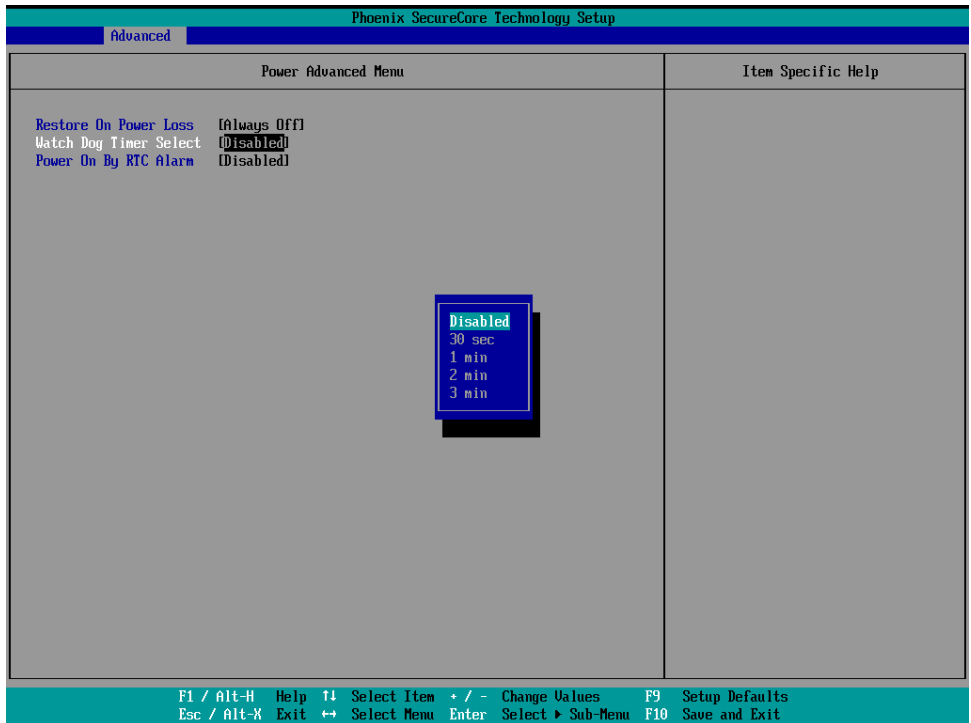
For further information, please refer to Nuvoton NCT6126D datasheet

Appendix C <Hardware Monitor>

Find the setting from

Advanced-> Motherboard Advanced menu-> Super IO configuration->

→Hardware Monitor



Appendix D <Programmable GPIO>

The GPIO can be programmed with the MS-DOS debug program using simple IN/OUT commands.

GPIO	0	1	2	3	4	5	6	7
bit	0	1	2	3	4	5	6	7

-o 4E 87 ;enter configuration
-o 4E 87
-o 4E 07
-o 4F 07 ;select Logical Device
-o 4E 30
-o 4F 18 ;activate GPIO function (The board use GPIO3)
-o 4E EC
-o 4F XX ;set "01" GPIO as input, set "00" GPIO as output
-o 4E ED
-o 4F XX ;if set GPIO as output, this register's value can be set "00~ FF"

Optional

-o 4E EE
-o 4F XX ;set "01", the respective bit are inverted (Both input and output)
 ;set "00", the respective bit are normal

For further information, please refer to Nuvoton NCT6126D datasheet

Contact information

Any advice or comment about our products and service, or anything we can help you please don't hesitate to contact with us. We will do our best to support you for your products, projects and business.

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