

DSP-L138



Features

- λ TI OMAP-L138 Dual-Core SoC
- λ 1x 456-MHz ARM926EJ-S RISC MPU
- λ 1x 456-MHz C674x Fixed- and Floating-Point VLIW DSP
- λ Integrated TLV320AIC3106 Stereo Audio CODEC
- λ Integrated XDS100 emulation
- λ Integrated LCD Control support
- λ 128 MB DDR2, 8GB SPI Flash
- λ 1 x SATA2, 1 x USB2.0 OTG, 1 x USB1.1 OHCI, 1 x COM, 1 x SDMEM/SDIO, 1 x 10Base-T/100Base-TX
- λ DC 5V input adapter

System

Processor	TI OMAP-L138 Dual-Core SoC: 1x ARM926EJ-S RISC MPU and 1x C674x Fixed and Floating-Point VLIW DSP
Speed	ARM926 @ 456 MHz C674x DSP @ 456 MHz
Memory	128 MB DDR2 SDRAM
Boot Mode	Configurable boot mode
Real Time Clock	Integrated RTC with onboard lithium battery
Embedded Debug	XDS100 emulation circuit (onboard)

Display

LCD Control	Maximum resolution is 1024 x 1024 pixels
Display	Default to 800 x 600 TFT LCD display panel
Touch	4-wire resistive 800 x 600 touch screen

Software Support

TI DVSDK	Linux 2.6.37 for ARM TI DSP/BIOS
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TI Chip Support Library
TI DSP Library

I/O

Flash Support	1 x 8GB SPI flash chip (onboard) 1 x MMC/SD card slot
Serial ATA	Support both SATA I and SATA II
Audio	TI TLV320AIC3106 Stereo Audio CODEC
LAN	EMAC provides 10Base-T and 100Base-TX
Internal I/O	1 x SATA2, 1 x RS232, 1 x USB2.0 OTG, 1 x USB1.1 OHCI, 1 x TFT/STN, 1 x MMC/SD
Debug I/O	XDS100 emulation (via USB interface)

Mechanical & Environmental

Power Requirement	DC Input 5V
Size & Thickness	170.8mm x 116.8mm (L x W)
Temperature	Operating within 0°C~60°C (32°F~140°F) Storage within -20°C~80°C (-4°F~176°F)
Relative Humidity	10%~90%, non-condensing

Ordering Information

Standard	DSP-L138 base board: 1x 8GB SD card with TI DVSDK image and stereo audio cables
Ordering code	DSP-L138: DSP-L138 board with stereo audio cables SPD-030-5 (option): A 5V AC/DC adapter D13-080SUTB00A0-S V1.0 (option): A 800x600 panel and touch screen and cables