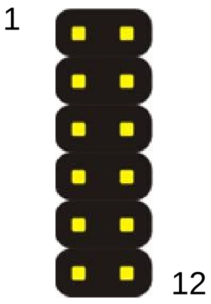
Trusted Platform Module (TPM)

LPC Interface

ADP-TPM

Complete solution implementing version 1.2 of the Trusted Computing Group specifications

IC Model	Infineon SLB9635 TT 1.2
Interface	12-Pin Low Pin Count
Features	TCG-compliant Trusted Platform Module Security architecture based on Infineon security controller family ROM for TCG firmware EEPROM for TCG firmware and data Hardware hash accelerator for SHA-1 algorithm Advanced Crypto Engine (ACE) for asymmetric key operations(up to 2048-bit key length) Memory encryption
OS Support	Windows XP, Vista, 7
Dimension	30 x 13mm
Ordering Code	
ADP-TPM	12-Pin TPM module, Firmware V3.19, Software management tool
ADP-TPM2E	Same as ADP-TPM, with base address 2E/2F



Pin Assignment

Pin	Description	Pin	Description
1	LPC_CLK	2	RESET-
3	-LFRAME	4	LAD3
5	LAD2	6	LAD1
7	LAD0	8	+3.3V
9	SERIRQ	10	Ground
11	+3.3VSB	12	N/C

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